Fuzzy Based Current Control Scheme for Harmonic Compensation in Grid Connected DG Units

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Abstract—In order to utilize DG unit interfacing converters to actively compensate distribution system harmonics, this paper proposes an enhanced fuzzy based current control approach. It seamlessly integrates system harmonic mitigation capabilities with the primary DG power generation function. As the proposed current controller has two well decoupled control branches to independently control fundamental and harmonic DG currents, phase-locked loops (PLL) and system harmonic component extractions can be avoided during system harmonic compensation. Moreover, a closed-loop power control scheme is also employed to derive the fundamental current reference. The proposed power control scheme effectively eliminates the impacts of steady-state fundamental current tracking errors in the DG units. Thus, an accurate power control is realized even when the harmonic compensation functions are activated. Simulation results from a single-phase DG unit validate the correctness of the proposed methods.

Abstract—active power filter, harmonic detection, phase-locked loop, and virtual impedance

I. INTRODUCTION

Due to the growing importance of renewable-energy-based power generation, a large number of power electronics interfaced DG units have been installed in the low-voltage power distribution systems [1]. It has been reported that the control of interfacing converters can introduce system resonance issues [2]. Moreover, the increasing presence of nonlinear loads, such as variable-speed drives, light-emitting diode (LED) lamps, compact fluorescent lamps (CFLs), etc., will further degrade distribution system power quality. To compensate distribution system harmonic distortions, a number of active and passive filtering methods have been developed [3]. However, installing additional filters is not very favorable due to cost concerns. Alternatively, distribution system power quality enhancement using flexible control of gridconnected DG units is becoming an interesting topic [5]–[6], where the ancillary harmonic compensation capability is integrated with the DG primary power generation function through modifying control references. This idea is especially attractive considering that the available power from backstage renewable energy resources is often lower than the power rating of DG interfacing converters. For the local load harmonic current compensation methods as discussed in [5]–[6], an accurate detection of local load harmonic current is important. Various types of harmonic detection methods [4] have been presented, such as the Fourier transformation based detection method in the detection scheme using instantaneous real and reactive power theory in [4], second-order generalized integrator (SOGI) in [1], and the delayed-signal-cancellation-based detection. Nevertheless, harmonic extraction process substantially increases the computing load of DG unit controllers. For a cost-effective DG unit with limited computing ability, complex harmonic extraction methods might not be acceptable. Alternatively, an interesting harmonic detectionless method was proposed . It shows that the main grid current can be directly controlled to be sinusoidal, instead of regulating DG output current to absorb local load harmonics. In this scenario, local load current is essentially treated as a disturbance in the grid current regulation loop. It should be noted that DG system normally has smaller stability margin when the direct control of grid current is employed. In addition,
for the shunt active harmonic filtering via point of connection (PoC) harmonic voltage detection (also named as resistive active power filter) the control techniques cannot be used. Alternatively, the recently proposed hybrid voltage and current control method (HCM) also allows the compensation of local load harmonics without using any harmonic detection process, where the well-understood droop control scheme is adopted to regulate the output power of the DG unit. Further considering that droop-control-based DG unit often features slow power control dynamics and current-controlled DG units are more widely installed in the distribution system, developing a robust current-control-based harmonic compensation method without using any system harmonic detection is very necessary.

It is worth mentioning that the DG real and reactive power control performance shall not be affected during the harmonic compensation. To satisfy this requirement, the fundamental DG current reference shall be calculated according to power references. Conventionally, the fundamental current reference can be determined based on the assumption of ripple-free grid voltage with fixed magnitude, and the PLL is used to synchronize the fundamental current reference with the main grid. However, considering that PoC voltage magnitude often varies due to the distribution system power flow fluctuations, this method may cause nontrivial power control errors. Alternatively, the fundamental current reference can also be calculated through the “power–current transformation” where only the detected PoC voltage fundamental component is used in the calculation. However, for a DG unit with the ancillary harmonic compensation capability, the interactions between distorted DG current and PoC harmonic voltages may contribute some DC real and reactive power bias and these power bias cannot be directly addressed in the control method. In order to ensure accurate power tracking performance, a closed-loop DG power control is necessary.

To simplify the operation of DG units with ancillary harmonic compensation capabilities while maintaining accurate power control, this paper proposes an improved current controller with two parallel control branches. The first control branch is responsible for DG unit fundamental current control, and the second one is employed to compensate local load harmonic current or feeder resonance voltage. In contrast to the conventional control methods with harmonic detection, the PoC voltage and local load current can be directly used as the input of the proposed current controller, without affecting the harmonic compensation accuracy of the DG unit. Moreover, with simple PI regulation in the outer power control loop, the proposed DG unit also achieves zero steady-state power tracking errors even when the fundamental current tracking has some steady-state errors. Simulated and experimental results from a single-phase DG unit validate the effectiveness of the proposed DG control method.

II. DG UNITS WITH HARMONIC COMPENSATION

In this section, a DG unit using the compensation strategies in the conventional active power filters is briefly discussed. Afterward, a detailed discussion on the proposed control strategy is presented.

A. Conventional Local Load Harmonic Current Compensation

Fig. 1 illustrates the configuration of a single-phase DG system, where the interfacing converter is connected to the distribution system with a coupling choke (L_f and R_f). There is a local load at PoC. In order to improve the power quality of grid current (I_2), the harmonic components of
local load current \((I_{\text{Local}})\) shall be absorbed through DG current \((I_1)\) regulation. The DG unit control scheme is illustrated in the lower part. As shown, its current reference consists of two parts. The first one is the fundamental current reference \((I_{\text{ref} f})\), which is synchronized with

\[
I_{\text{ref} f} = \frac{\cos(\theta) \cdot P_{\text{ref}} + \sin(\theta) \cdot Q_{\text{ref}}}{E^*} 
\]

(1)

where \(\theta\) is the PoC voltage phase angle detected by PLL, \(P_{\text{ref}}\) and \(Q_{\text{ref}}\) are the real and reactive power references, and \(E^*\) is the nominal voltage magnitude of the system. However, the current reference generator in (1) is not accurate in controlling DG power, due to variations of the PoC voltage magnitude. To overcome this drawback, an improved power control method with consideration of PoC voltage magnitude fluctuations was developed as shown in Section II-B. First, the fundamental PoC voltage \(V_{\text{PoC} \alpha f}\) and its orthogonal component \(V_{\text{PoC} \beta f}\) (quarter cycle delayed respect to \(V_{\text{PoC} \alpha f}\)) are obtained by using SOGI as

\[
V_{\text{PoC} \alpha f} = \frac{2\omega D_1 s}{s^2 + 2\omega D_1 s + \omega_f^2} \cdot V_{\text{PoC}} 
\]

(2)

\[
V_{\text{PoC} \beta f} = \frac{2\omega D_1 \omega_f}{s^2 + 2\omega D_1 s + \omega_f^2} \cdot V_{\text{PoC}} 
\]

(3)

where \(\omega D_1\) is the cutoff bandwidth of SOGI and \(\omega_f\) is the fundamental angular frequency. For a single-phase DG system, relationships between the power reference and the fundamental reference current can be established in the artificial stationary \(\alpha - \beta\) reference frame as follows:

\[
P_{\text{ref}} = \frac{1}{2} \cdot (V_{\text{PoC} \alpha f} \cdot I_{\text{ref} \alpha f} + V_{\text{PoC} \beta f} \cdot I_{\text{ref} \beta f}) 
\]

(4)

\[
Q_{\text{ref}} = \frac{1}{2} \cdot (V_{\text{PoC} \beta f} \cdot I_{\text{ref} \alpha f} - V_{\text{PoC} \alpha f} \cdot I_{\text{ref} \beta f}) 
\]

(5)

where \(I_{\text{ref} \alpha f}\) and \(I_{\text{ref} \beta f}\) are the DG fundamental current reference and its orthogonal component in the artificial \(\alpha - \beta\) reference frame. Similarly, \(V_{\text{PoC} \alpha f}\) and \(V_{\text{PoC} \beta f}\) are PoC fundamental voltage and its orthogonal component, respectively. According to (4) and (5), the instantaneous fundamental current reference \((I_{\text{ref} f})\) of a single-phase DG unit can be obtained as

\[
I_{\text{ref} f} = I_{\text{ref} \alpha f} = \frac{2(V_{\text{PoC} \alpha f} \cdot P_{\text{ref}} + V_{\text{PoC} \beta f} \cdot Q_{\text{ref}})}{P_{\text{ref}} + \frac{V_{\text{PoC} \alpha f}^2}{2} + \frac{V_{\text{PoC} \beta f}^2}{2}}. 
\]

(6)

Moreover, to absorb the harmonic current of local nonlinear load, the DG harmonic current reference \((I_{\text{ref} h})\) is produced

\[
I_{\text{ref} h} = G_D(s) \cdot I_{\text{local}} = \sum_{h=1,3,5,...} \frac{\omega D_2 s}{s^2 + 2\omega D_2 s + \omega_h^2} \cdot I_{\text{Local}} 
\]

(7)

where \(GD(s)\) is the transfer function of the harmonic extractor. To realize selective harmonic compensation performance \(GD(s)\) is designed to have a set of bandpass filters with cutoff frequency \(\omega D_2\). With the derived fundamental and harmonic current references, the DG current reference is written as \(I_{\text{ref}} = I_{\text{ref} f} + I_{\text{ref} h}\). Afterward, the proportional and multiple resonant controllers [12], [18]–[20] are adopted to ensure rapid current tracking

\[
V_{\text{PWM}} = G_{\text{cur}}(s) \cdot (I_{\text{ref}} - I_1) 
\]

\[
= \left(K_p + \sum_{h=1,3,5,...,15} \frac{2K_{ih}\omega_h s}{s^2 + 2\omega_h s + \omega_h^2} \right) \cdot (I_{\text{ref} f} + I_{\text{ref} h} - I_1) 
\]

(8)

where \(P\) is the reference voltage for pulswidth modulation (PWM) processing, \(K_p\) the proportional gain of the current controller, \(K_{ih}\) the resonant controller gain at the order \(h\), \(\omega_c\) the cutoff frequency of the resonant controller, and \(\omega_h\) is the angular frequency at fundamental and selected harmonic frequencies.

B. Conventional Feeder Resonance Voltage Compensation

It should be pointed out that the objective of local load harmonic compensation is to ensure sinusoidal grid current \(I_2\) in Fig. 1. In this control mode, DG unit should not actively regulate the PoC voltage quality. As a result, the PoC voltage can be distorted especially when it is connected to the main grid through a long underground cable with nontrivial parasitic capacitance [8], [23]. In this case, the feeder is often modeled by an LC ladder [23], [26]. To address the resonance issue
associated with long underground cables, the R-APF concept can also be embedded in the DG unit current control, as illustrated in Fig. 2. Compared to Fig. 1, the DG harmonic current reference in this case is modified as

\[ I_{\text{ref},h} = \left( \frac{1}{R_V} \right) \cdot (G_D(s) \cdot V_{\text{POC}}) \]  

where \( R_V \) is the virtual damping resistance at harmonic frequencies. With this harmonic current reference (9), the DG unit essentially works as a small equivalent harmonic resistor at the end of the feeder, when it is viewed at power distribution system level. By providing sufficient damping effects to the long feeder, the voltage quality at different positions of the feeder can be improved.

C. Proposed Harmonic Compensation Method

Note that for the local load harmonic current compensation and the PoC harmonic voltage compensation, the harmonic current is absorbed by the DG unit. Consequently, interactions between DG harmonic current and PoC harmonic voltage may cause some steady-steady DG power offset. Nevertheless, the power control using fundamental current reference in (6) is still in an open-loop manner, which can not address the power offset introduced by harmonics interactions. In order to achieve accurate power control performance in current-controlled DG units, the instantaneous fundamental current reference (including both magnitude and phase angle information) can be determined by a simple closed-loop power control strategy as

\[ I_{\text{ref}} = g_1 \cdot V_{\text{POCa}} + g_2 \cdot V_{\text{POCb}} \]  

where \( V_{\text{POCa}} \) is the nonfiltered PoC voltage expressed in the \( \alpha - \beta \) reference frame (\( V_{\text{POCa}} = V_{\text{POc}} \)) and \( V_{\text{POCb}} \) is its orthogonal component. The gains \( g_1 \) and \( g_2 \) are adjustable and they are used to control DG unit real and reactive power, respectively. The detailed regulation law is shown as follows:

\[ g_1 = \left( k_{p1} + \frac{k_{I1}}{s} \right) \cdot \left( \frac{1}{\tau s + 1} \cdot P_{\text{ref}} - P_{\text{DG}} \right) + \frac{P_{\text{ref}}}{(E^*)^2} \]  
\[ g_2 = \left( k_{p2} + \frac{k_{I2}}{s} \right) \cdot \left( \frac{1}{\tau s + 1} \cdot Q_{\text{ref}} - Q_{\text{DG}} \right) + \frac{Q_{\text{ref}}}{(E^*)^2} \]

where \( k_{p1} \), \( k_{I1} \), \( k_{p2} \), and \( k_{I2} \) are proportional and integral control parameters, \( P_{\text{ref}} \) and \( Q_{\text{ref}} \) are the real and reactive power references, \( E^* \) is the nominal voltage magnitude of the DG unit, \( \tau \) is the time constant of first-order low-pass filters. \( PDG \) and \( QDG \) are measured DG power with low-pass filtering as

\[ P_{\text{DG}} = \frac{1}{2(\tau s + 1)} \cdot (V_{\text{POCa}} \cdot I_{1\alpha} + V_{\text{POCb}} \cdot I_{1\beta}) \]  
\[ Q_{\text{DG}} = \frac{1}{2(\tau s + 1)} \cdot (V_{\text{POCb}} \cdot I_{1\alpha} - V_{\text{POCa}} \cdot I_{1\beta}) \]

where \( I_{1\alpha} \) is the nonfiltered DG current expressed in the stationary \( \alpha - \beta \) frame (\( I_1 = I_{1\alpha} \)) and \( I_{1\beta} \) is its delayed orthogonal component. Note that in (13) and (14), the power offset caused by harmonic voltage and harmonic current interactions is also considered. Although the proposed closed-loop power control method eliminates power tracking errors, it can be seen that the fundamental current reference in (10) will be distorted if PoC voltage has some ripples. When it is applied to the current controller in (8), the distorted fundamental current reference will affect the performance of DG harmonic current tracking.

To overcome this drawback, an improved proportional and resonant controller with two control branches is proposed as

\[ V_{\text{PWM}} = \frac{2K_{\text{f1}}s\omega_h}{s^2 + 2\omega_h s + \omega_h^2} \cdot (I_{\text{ref},h} - I_1) \]

\[ + \left( K_p + \sum_{h=3,5,7,11} \frac{2K_{\text{f1}}s\omega_h}{s^2 + 2\omega_h s + \omega_h^2} \right) \cdot (I_{\text{ref},h} - I_1) \]

\[ = G_f(s) \cdot (I_{\text{ref},h} - I_1) + G_h(s) \cdot (I_{\text{ref},h} - I_1) \]

As shown, the fundamental current reference in (10) is regulated by the “power control” branch in (15). As only fundamental resonant controller is adopted in this branch, the impacts of harmonic components in \( I\text{ref} \) can be automatically filtered out. Therefore, the power control branch will not introduce any obvious harmonic disturbances to the harmonic control branch in (15). Meanwhile, the harmonic current reference \( I_{\text{ref},h} \) is regulated by the “harmonic control” branch, where only harmonic resonant controllers are included. Considering some noncharacteristic harmonics exist in the system, a small proportional gain \( K_p \)
Fuzzy Based Current Control Scheme

is used to ensure superior harmonic current tracking. As the fundamental resonant controller is not included in the harmonic control branch, it is possible to remove the harmonic extraction blocks in (7). Accordingly, the local load current or PoC voltage without any filtering is directly used as the input of the harmonic control branch. Note that when the harmonic current reference $I_{ref\,h}$ is set to zero, the harmonic control branch ensures that the DG current is ripple-free. This is very similar to the performance of conventional DG unit without any compensation, where the DG unit current is controlled to be sinusoidal. In summary, the harmonic current reference in (15) can have three options as follows:

$$I_{ref\,h} = \begin{cases} I_{\text{local}}, & \text{Local nonlinear load compensation} \\ -\frac{V_{\text{PoC}}}{R_c}, & \text{Feeder resonance voltage compensation} \\ 0, & \text{DG harmonic current rejection} \end{cases} \quad (16)$$

With the proposed method in (15) and the control reference in (16), it seems there is a complication. When the local load current or the PoC voltage are directly employed as the input of the harmonic control branch [see (16)], the proportional gain $K_p$ in (15) makes the output of harmonic control branch has some fundamental components. These fundamental components may cause interference with the power control branch. As a result, the fundamental current tracking will have steady-state errors. However, further considering that the fundamental current tracking in (15) essentially behaves as an inner loop of the proposed closed-loop DG power ($P_{DG}$ and $Q_{DG}$) regulation in (10), the DG unit still has zero steady-state power control error even when its fundamental current tracking has some errors. The diagram of the proposed control method is presented in Fig. 3. It shows that the PLL and harmonic detection as shown in the conventional harmonic compensation schemes in (7) and (9) are removed from the proposed DG controller. Also an accurate real and reactive power control is guaranteed by two PI regulators in the power control loop. It is important to mention that grid voltage frequency variations [35] often affect the power control accuracy and the harmonic compensation performance of the DG unit. As PI controllers are used in the closed loop power control in (11) and (12), zero steady-state power control error can still be realized even when grid voltage frequency has variations. On the other hand, to alleviate the impact of frequency variation to DG harmonic current tracking accuracy, wide bandwidth ($\omega_c$) harmonic resonant controllers should be considered in (15). Additionally, if the DG unit needs to be connected to a utility grid with nontrivial frequency variations, a frequency estimator can be adopted to detect the frequency of the grid. In this case, the detected grid frequency is used as a parameter of resonant controllers and the DG current tracking accuracy can be improved accordingly. Note that the frequency detector, such as that in [36], is simpler than a PLL, as the grid angle calculation is not needed in the proposed DG power control loop.

III. MODELING OF DG UNIT WITH THE PROPOSED CURRENT CONTROL SCHEME

In this section, the harmonic compensation performance using the proposed current controller is investigated.

A. Modeling of the Proposed Current Control Method

It is well understood that the current-controlled inverter shall be described as a closed-loop Norton equivalent circuit [27], [29]

$$I_I = H_c(s) \cdot I_{\text{ref}} - Y_c(s) \cdot V_{\text{PoC}} \quad (17)$$

where the gain ($H_c(s)$ and $Y_c(s)$) can be derived based on the conventional current controller in (8) and the DG unit circuitry parameters [27]. The corresponding equivalent circuit is shown in Fig. 4. Note that for the DG unit with harmonic compensation capability, the current reference $I_{\text{ref}}$ in Fig. 4 has two components ($I_{\text{ref}}$ and $I_{\text{ref\,h}}$). For the DG unit using the proposed current control method, its equivalent circuit is derived as shown in the rest of this section. First, (18) describes the transfer function of DG unit filter plant $G_{\text{Ind}}(s)$ as

$$I_1 = G_{\text{Ind}}(s) \cdot (V_{\text{PWM}} - V_{\text{PoC}}) = \frac{1}{L_f s + R_f} \cdot (V_{\text{PWM}} - V_{\text{PoC}}) \quad (18)$$

where $L_f$ is the inductance of the DG coupling choke and $R_f$ is its stray resistance. $V_{\text{PWM}}$ is the
average inverter output voltage. Additionally, the delay of DG control [28] is written as

\[ V_{PWM} = e^{-1.5T_d s} \cdot V_{PWM} \]  \hspace{1cm} (19) \]

where \( T_d \) is the sampling period of the system. Note that the delay here includes one sampling period processing delay and half sampling period voltage modulation delay. By solving (15), (18), and (19), the closed-loop DG current response can be given as

\[ I_1 = H_f(s) \cdot I_{ref f} + H_h(s) \cdot I_{ref h} - Y_P(s) \cdot V_{PoC} \]  \hspace{1cm} (20) \]

where \( H_f(s) \) and \( H_h(s) \) represent the closed-loop response of DG unit current to fundamental and harmonic current references, respectively. \( Y_P(s) \) demonstrates the sensitivity of DG line current tracking to PoC voltage disturbances [27]. The detailed expression of terms in (20) is listed as

\[ H_f(s) = \frac{e^{-1.5T_d s} \cdot G_f(s) \cdot G_{ind}(s)}{1 + e^{-1.5T_d s} \cdot (G_f(s) + G_h(s)) \cdot G_{ind}(s)} \]
\[ H_h(s) = \frac{e^{-1.5T_d s} \cdot G_h(s) \cdot G_{ind}(s)}{1 + e^{-1.5T_d s} \cdot (G_f(s) + G_h(s)) \cdot G_{ind}(s)} \]
\[ Y_P(s) = \frac{G_{ind}(s)}{1 + e^{-1.5T_d s} \cdot (G_f(s) + G_h(s)) \cdot G_{ind}(s)}. \]

For the DG unit with the proposed current control scheme, a modified Norton equivalent circuit with two controlled current sources can be applied to demonstrate the unique behavior of the proposed controller. As illustrated in Fig. 5, the current source \( H_f(s) \cdot I_{ref f} \) is responsible for regulating DG unit fundamental current. Additionally, the current source \( H_h(s)\cdot I_{ref h} \) aims to compensate system harmonics at selected harmonic frequencies.

IV. FUZZY CONTROLLER

The disadvantage of PI controller is its inability to react to abrupt changes in the error signal, \( \varepsilon \), because it is only capable of determining the instantaneous value of the error signal without considering the change of the rise and fall of the error, which in mathematical terms is the derivative of the error denoted as \( \Delta \varepsilon \). To solve this problem Fuzzy logic control as it is shown in Fig 2 is proposed.
All the variables’ fuzzy subsets for the inputs ε and Δε are defined as (NB, NM, NS, Z, PS, PM, PB). The membership function of inputs is illustrated in fig.4&5. The fuzzy control rule is illustrated in the table II.

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Table II FLC rule base.

V. SIMULATION RESULTS

In order to verify the correctness of the proposed control strategy, simulated and experimental results are obtained from a single-phase DG unit.

A. Simulated Results

1) Compensation of Local Nonlinear Loads: First, the DG unit with a local diode rectifier load is tested in the simulation. The configuration of the system is the same as shown in Fig. 1, and PoC is connected to a stiff controlled voltage source (to emulate the main grid) with nominal 50 Hz frequency. The main grid voltage contains 2.8% third and 2.8% fifth harmonic voltages. In this simulation, the reference power is set to 600W and 200 var. The detailed parameters of the system are provided in Table I. When the local load harmonic current is not compensated by the DG unit [corresponding to Iref h = 0 in (15) and (16)], the performance of the DG unit is shown in Fig. 9. It can be seen from Fig. 9(b) that the DG current is sinusoidal with 5.57% total harmonic distortion (THD). At the same time, the harmonic load currents flow to the main grid is illustrated in Fig. 9(a). Once the local load harmonic current compensation is activated by setting Iref h = ILocal in (16), the performance of the system is shown in Fig. 10. Although harmonic extractions are not used in this simulation, the proposed method can still realize satisfied local load harmonic current compensation, resulted in an enhanced grid current quality with 5.88% THD. Meanwhile, DG unit current is polluted with 201.5% THD. For the DG unit operating under local load harmonic compensation mode, its fundamental current reference adjusted by (10) is shown in Fig. 11(a). As the DG unit also provides 200 var reactive power to the grid, it can be seen that the fundamental current reference is slightly lagging of the PoC voltage. The effectiveness of the proposed closed-loop power control strategy is verified in Fig. 12, where the real and reactive power is calculated by (13) and (14). When the conventional open-loop power control in (6) is applied, it can be noticed that the DG output real and reactive power control is not accurate. On the other hand, as the proposed control strategy regulates DG output power in a closed-loop manner, it guarantees zero steady-state power tracking error.
2) Performance Under Frequency Disturbance:
The performance of the DG unit under grid voltage frequency deviation is also examined. In this test, the bandwidth ($\omega_c$) of resonant controllers at harmonic frequencies is selected as 16 rad/s. As a result, the performance of current tracking can be less sensitive to grid voltage frequency variations. In Fig. 13, the DG unit power reference is 600 W/600 var and the grid voltage frequency is fixed to 50 Hz before 1.0 s. During this time range, it can be seen that DG unit absorbs the harmonic current from local nonlinear loads and the grid current THD is only 5.05%. At the time instant 1.0 s, the grid frequency jumps to 52 Hz. In the case of grid frequency variation, it can be seen that the proposed method still maintains satisfactory harmonic compensation performance with 5.99% grid current THD. It is emphasized here that in a real DG system, the frequency deviation is typically lower, e.g., for the small photovoltaic (PV) systems, the allowed frequency deviation range is –0.7 to 0.5 Hz. The DG unit needs to be disconnected from the utility when the grid frequency deviation is out of this range [35]. If very large frequency variation is present, a frequency estimator could be used to update the PR control parameters. As discussed earlier, such a frequency estimator will be simpler than a PLL.
3) Compensation of Feeder Resonance Voltage: To verify the feasibility of the proposed method in compensating feeder resonance voltages, the DG unit is connected to the stiff main grid with five cascaded LC filters (see Fig. 2). The inductance and capacitance of each LC filter is 1 mH and 25 \( \mu \)F, respectively. The performance of this system is shown in Figs. 14–18. The performance of the proposed controller under DG unit harmonic rejection mode \((I_{\text{ref}} h = 0)\) is given in Figs. 14 and 15. As shown in the upper part of Fig. 14, the PoC voltage is distorted with 13.6% THD, due to the resonance aggregated in the \( LC \) ladder. Since the fundamental current \((I_{\text{ref} f})\) is synchronized with the nonfiltered PoC voltage and its orthogonal component, it is also distorted as presented in the lower part of Fig. 14. Although the fundamental current reference derived by (10) is distorted, it can be seen from Fig. 15(b) that the DG current is sinusoidal with 5.61% THD. Meanwhile, the main grid current contains nontrivial harmonics with 34.2% THD.

When the feeder resonance voltage compensation is enabled by controlling the DG unit as a virtual resistance \([RV = 5 \text{ in (16)}]\) at selected harmonic frequencies, corresponding responses of the system are shown in Figs. 16 and 17. In contrast to the performance in Fig. 14, it can be seen from Fig. 16 that the PoC harmonic voltage in this case is mitigated and its THD reduces to 3.07%.

The associated current waveforms during feeder resonance voltage compensation are shown in Fig. 17. It is obvious that DG current has more distortions (with 35.09% THD), while the main grid current THD reduces to 8.12%. Finally, the power flow performance of the DG unit using the proposed power control scheme is shown in Fig. 18. From the time range 0 to 1.0 s, the DG unit is controlled to eliminate DG harmonic currents \((I_{\text{ref}} h = 0)\). From 1.0 to 1.5 s, feeder resonance voltage compensation is slowly activated by changing \( RV \) from infinity to 5 \( \Omega \). It can be seen that the power control is always accurate during the transitions between different control modes.

V. CONCLUSION

In this paper, a simple harmonic compensation strategy is embedded in the DG unit interfacing converters. By breaking the conventional proportional and multiple resonant controllers into two parallel control branches, the proposed method realizes distribution system harmonic compensation without using any phase-locked loops or system harmonic extractions. Moreover, the input of the fundamental power control branch is regulated by fuzzy based controllers, which ensure an accurate power control even when the system harmonic compensation tasks are activated in the DG unit or the PCC voltage changes.

REFERENCES


